

FORM PTO-1449  
(Modified)U.S. Department of Commerce  
Patent and Trademark Office

Attorney Docket No.: SONY-12200

Serial No.: 09/611,766

**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**  
(Use Several Sheets If Necessary)

Applicant: David V. James et al.

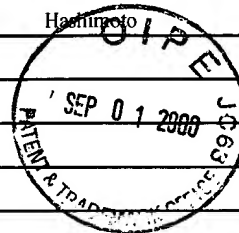
(37 CFR § 1.98(b))

Filing Date: July 6, 2000

Group Art Unit:

## U.S. PATENT DOCUMENTS

Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date
CH	AA	6,034,964	03/07/00	Fukushima et al.	370	401	06/06/97
CH	AB	5,941,964	08/24/99	Young et al.	710	100	06/07/95
CH	AC	5,517,494	05/14/96	Green	370	60	09/30/94
CH	AD	5,815,668	09/29/98	Hamamoto	395	200.68	03/15/96
	AE						
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Examiner:

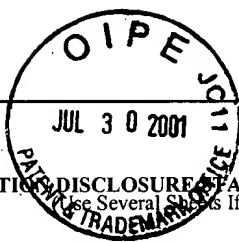
CHUONG HU

Date Considered:

07/25/04

**EXAMINER:**

Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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INFORMATIONAL DISCLOSURE STATEMENT BY APPLICANT  
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Applicant: David V. James et al.

AUG 02 2001

Filing Date: July 6, 2000

Technology Center 2600731

## U.S. PATENT DOCUMENTS

Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date
CH	AA	5,402,419	03/28/95	Osakabe et al.	370	85.1	12/17/93
CH	AB	5,485,458	01/16/96	Oprescu et al.	370	85.2	03/05/93
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## FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

		Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation	
							Yes	No
	AK							
	AL							
	AM							
	AN							
	AO							

## OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

CH	AP	Michael Teener et al., "A Bus on a Diet - The Serial Bus Alternative, An Introduction to the P1394 High performance Serial Bus" Apple Computer, Inc. Santa Clara, CA, Pub. Date.: 02/24/92, pgs. 316-321.
CH	AQ	P1394 Standard For A High Performance Serial Bus," IEEE, 1995.
CH	AR	"The IEEE-1394 High Speed Serial Bus," R.H.J. Bloks, Philips Journal Of Research, Vol.50, No. 1/2, pp. 209-216, 1996.
CH	AS	SPI, "Architecture for High Performance Transparent Bridges" July, 1992, IBM Corporation, 3 pages.
CH	AT	"Digital Interface for Consumer Audio/Video Equipment" The Netherlands, 46 pages.
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Examiner: CHUONG HO

Date Considered: 07/25/04

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